

Receipt

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:



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EXAMINER:

5 / CFR  
E. Willis  
7-9-02

Serial No.: 10/072,734

Art Unit: 2858

Filing Date: February 5, 2002

For: METHOD FOR TESTING SEMICONDUCTOR WAFERS

Attorney Docket No. 96-750.3

**REQUEST FOR CORRECTION ON THE FILING RECEIPT**  
**May 22, 2002**

APPLICATION PROCESSING DIVISION'S  
CUSTOMER CORRECTION BRANCH  
Patent & Trademark Office  
Washington, D.C. 20231

Dear Sir:

It is respectfully requested to correct the Filing Receipt in the above application. The Attorney Docket Number should be 96-750.3. A copy of the Filing Receipt with the correction noted thereon is enclosed.

If any fees are due, the Commissioner is authorized to deduct any fees from Deposit Account No. 07-1857. A duplicate copy of this sheet is enclosed.

Respectfully submitted:

  
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**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: APPLICATION PROCESSING DIVISION'S CUSTOMER CORRECTION BRANCH, Patents and Trademark Office, Washington, D.C. 20231 on this 22nd day of May, 2002.

May 22, 2002  
Date of Signature

  
Stephen A. Gratton, Attorney for Applicants



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| APPLICATION NUMBER | FILING DATE | GRP ART UNIT | FIL FEE REC'D | ATTY DOCKET NO.   | DRAWINGS | TOT CLAIMS | IND CLAIMS |
|--------------------|-------------|--------------|---------------|-------------------|----------|------------|------------|
| 10/072,734         | 02/05/2002  | 2858         | 1118          | <del>96-750</del> | 6        | 27         | 6          |

96-750.3

CONFIRMATION NO. 2695

022823

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FILING RECEIPT



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Date Mailed: 04/23/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

**Applicant(s)**

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**Assignment For Published Patent Application**

Micron Technology, Inc.;

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**Domestic Priority data as claimed by applicant**

THIS APPLICATION IS A DIV OF 09/929,388 08/14/2001 PAT 6,359,456  
WHICH IS A DIV OF 09/303,367 04/30/1999 PAT 6,275,052  
WHICH IS A DIV OF 08/797,719 02/11/1997 PAT 6,060,891

**Foreign Applications**

If Required, Foreign Filing License Granted 04/18/2002

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

## Title

Method for testing semiconductor wafers

## Preliminary Class

324

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**Title 37, Code of Federal Regulations, 5.11 & 5.15**

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